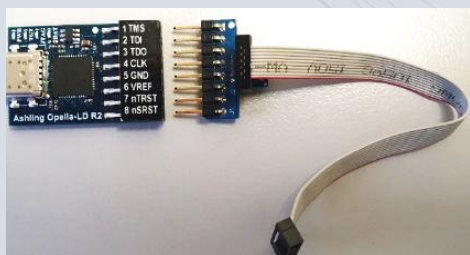




Opella-LD is Ashling's low-cost Debug Probe for embedded development with support for multiple target architectures including RISC-V & Arm powered systems.

Features

- **Opella-LD** software debug support is provided via the open-source [OpenOCD](#) standard & provides fast code download to the target system with control & interrogation support for all core-processor & system resources including registers & memory. **Opella-LD** supports hardware debugging using Ashling's [RiscFree™](#) SDK or the GCC GDB debugger.
- Other features include:
 - Low-cost, compact form-factor.
 - Works with RISC-V & Arm powered systems.
 - Works under Windows & Linux with a USB Type-C host PC interface.
 - Supports JTAG, cJTAG & CoreSight™ DAP & SWD debug interfaces with up to 30MHz data clock rates.
 - Four LEDs to indicate Power, Target Present, JTAG or SWD mode & Data Transfer Activity
 - Supports & automatically adapts to target voltages between 0.9v to 5.0v.
 - Supports JTAG TAP Reset (TRST) & System Reset (SRST).
 - Dedicated Ashling [support](#) site with latest user documentation, drivers & OpenOCD configuration files for all target architectures.



Opella-LD is provided with “flying-leads” allowing easy connection to your target. Dedicated adapters are also provided for the MIPI/Arm CoreSight 10-way target footprint as shown here to the left.



Product
Opella-LD Debug Probe (comes with flying leads and 10-way 0.05" adapter for MIPI/CoreSight targets)

Order Code
OPELLA-LD